



RESEARCH ARTICLE

A NEW ICI CANCELLATION SCHEME AND PERFORMANCE ANALYSIS OF BIT ERROR RATE FOR MIMO-OFDM BASED SYSTEMS

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ABSTRACT

A joint design of sphere decoding and synchronization algorithms for multiple-input-multiple-output (MIMO) orthogonal frequency-division multiplexing (OFDM) systems with progressive parallel inter-carrier interference canceller (PPIC) based on factor graph based soft self-iterative equalization in wireless multipath channels for high speed wireless transmission is proposed. This algorithm can suppress inter-antenna interferences and cancel inter-carrier interferences iteratively and progressively. With a proper designed message passing schedule and random interleaver, the short cycle problem is solved. The proposed PPIC is superior to PIC both in computational complexity and system architecture. It is very suitable for VLSI implementation and it is a potential candidate for data detection/decoding in future high data rate, and high mobility, We consider the performance analysis and design optimization of low density parity check (LDPC) coded multipleinput- multiple-output (MIMO) orthogonal frequency-division multiplexing (OFDM) systems for high speed wireless transmission.

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INTRODUCTION

MIMO OFDM is the corner stone of future broadband wireless access. Wide band transmission with high spectral efficiency and high mobility is required for future mobile radio communications. In a MIMO system, as data are transmitted/received through different antennas, many channel impairments need to be dealt with, such as multipath fading, AWGN noise, inter-antenna interference etc. To deal with these channel impairments, many types of MIMO detectors such as MAP detector [1], sphere decoder [2], MMSE-SIC detector [1], [3], etc. have been proposed. For OFDM-based systems, the transmission bandwidth is divided into many narrow subchannels, which are transmitted in parallel. Channel variations during one OFDM symbol leads to loss of subchannel orthogonality known as Inter Sub-Carrier Interference (ICI) which degrades the performance, since ICI can be seen as additional near-Gaussian noise. As delay spread increases, symbol duration should also increase in order to maintain a nearly flat channel in every frequency subband. As a result, the ICI effect becomes more severe as mobile speed, carrier frequency, and OFDM symbol duration increases. If it is not compensated, ICI will result in performance loss and an error floor that increases with Doppler frequency. In some circumstances, the ICI effect may degrade the BER performance significantly [4]–[6]. In [9] and [10], the well-known ICI self-cancellation scheme is proposed. By appropriately mapping symbols to a group of subcarriers,

the proposed algorithm in [9] makes OFDM transmissions less sensitive to the ICI at the cost of much lower bandwidth efficiency. In [11], it is shown that ICI power comes mainly from 12 neighboring subcarriers and based on this observation a low-complexity MMSE equalizer is proposed. Yet, as the MMSE equalizer still exhibits an error floor on BER, a low-complexity decision feedback equalizer (DFE) is proposed in [11].

ICI reduction method is proposed based on a sphere decoding (SD) algorithm. By considering channel information, a new search strategy is developed to reduce the computational complexity of the SD algorithm. In recent years, the message passing data detector/decoder catches the attention of many researchers. One appealing practical aspect of the message passing data detector/decoder is due to that it consists of many small, independent detection/decoding functions to deal with channel impairments. Hardware could be implemented according to these independent detectors/decoders and operated in parallel, and it potentially leads to a very-high-speed detector/decoder. This aspect is particularly important in data transmissions where data rate requirements are high, and processing delay must be low [14]. Some researchers pointed out that message passing algorithms can be realized with simple analog transistor circuits. The attraction of such analog detector/decoder comes from the fact that the iteration operation is actually the transient response. A popular message passing algorithm on factor graphs is the sum-product algorithm, which efficiently computes all the marginals of the

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individual variables of the function. Based on Factor Graph, a joint design of message passing MIMO data detector/decoder with a progressive parallel intercarrier interference canceller (PPIC) for OFDM based wireless communication systems is proposed in this paper.

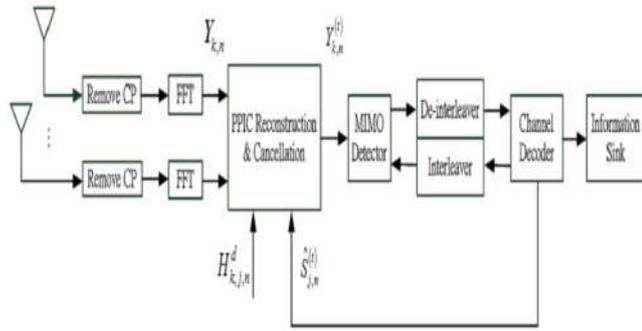


Fig. 1. Block diagram of the proposed message passing data detection/decoding and ICI cancellation scheme

The message type chosen in this work is log-likelihood ratio (LLR) of bit probabilities for the MIMO data detector/decoder and soft data symbols for the PPIC ICI canceller. The proposed algorithm detects the transmitted data iteratively, by jointly dealing with channel fading effects, AWGN noise and interferences in time domain, frequency domain and space domain. With the insertion of cyclic prefix in OFDM, the time domain ISI can be avoided. With the message passing MIMO detector (denoted as MPD in the following sections), the space domain interantenna interference can be suppressed and with the aid of PPIC, the frequency domain ICI can be cancelled. The computational complexity of the proposed PPIC architecture is relatively lower than the standard PIC architecture. The system architecture is also simpler and more suitable for the VLSI implementation. This paper is organized as follows: section II defines the system model of a wireless MIMO-OFDM communication system with ICI effects. The proposed message passing algorithm for LDPC-coded MIMO-OFDM systems is derived in section III. The Progressive PIC architecture is depicted in section IV. In section V, we discuss the schedule of message passing and the method to use interleaving to deal with the short-cycle problem. Section VI discusses the computational complexity and system architecture of the proposed algorithm. Simulation results of BER performance are given in section VII, and finally, in section VIII we conclude this paper.

SYSTEM MODEL

The channel state information (CSI) refers to known channel properties of a communication link. The CSI makes it possible to adapt transmissions to current channel conditions, which is crucial for achieving reliable communication with high data rates in multiantenna systems. Here, we assume perfect timing synchronization and both perfect and imperfect channel estimation in an OFDM-based wireless MIMO communication system with N_t transmit and N_r receive antennas. The transmitted time domain signal can be represented by the following equation:

$$S_{j,i} = \frac{1}{N_c} \sum_{n=0}^{N_c-1} S_{j,n} e^{j \frac{2\pi n i}{N_c}} \quad (1)$$

Where N_c is the FFT size, $S_{j,i}$ the symbol transmitted on the j th antenna and n th subcarrier and belonging to the constellation S with size $|S| = 2^m$, m is the modulation order, $S_{j,i}$ is the i th sample of the time domain signal transmitted on the j th antenna. The cyclic prefix vector can be represented as:

$$S_{CP,j}(i) = S_{j,N_c-N_G+i} \quad (2)$$

The i th sample of the received time domain signal at the k th antenna can be derived as:

$$Y_{k,i} = \sum_{j=0}^{N_c-1} \sum_{l=0}^{N_G} h_{k,j,l}^{(i)} S_{j,((i-l)N_c)} + Z_{k,i} \quad (3)$$

Then, the ICI channel coefficients in the frequency domain can be reformulated as:

$$H_{k,j,n}^{(d)} = \frac{1}{N_c} \sum_{l=0}^{N_G} F_l(d) e^{-j \frac{2\pi l(n-d)}{N_c}} \quad (4)$$

$$H_{k,j,n}^{(0)} = \frac{1}{N_c} \sum_{l=0}^{N_G} \sum_{i=0}^{N_c-1} h_{k,j,l}^{(i)} e^{-j \frac{2\pi n l}{N_c}} = \sum_{l=0}^{N_G} h_{k,j,l}^{ave} e^{-j \frac{2\pi n l}{N_c}} \quad (5)$$

where $n, d = 0 \sim N_c - 1$, have k, j, l is average of the l th channel tap over the useful time duration of an OFDM symbol. Without loss of generality, in the following sections, only the n th subcarrier of the MIMO-OFDM receiver is considered.

MESSAGE PASSING ALGORITHM AND LDPC DECODER

A. Message Passing MIMO Detector (MPD)

Without consideration of PPIC, to describe the function of an MPD for a MIMO-OFDM receiver, first, a factor graph for the 2×2 MIMO flat fading channel with QPSK modulation is constructed. The messages passed between variables are log-likelihood ratios of bit probabilities, $LQ = \ln[P(bq = 0)/P(bq = 1)]$. The transmit variable bq , defined as bit node on the factor graph, and the receive variable Yk , defined as channel node on the factor graph, generate messages using sum-product rule [15]. Let b represent bits $(b_0, \dots, b(mN_t-1))T$. The message generated by Yk , and passed to bq , called $(k \rightarrow q)$ is

$$L_{R(k \rightarrow q)} = \ln \frac{\sum_{b: b_q=0} \left\{ p\left(\frac{Y_{k,n}}{b}\right) \exp\left[\sum_{r=0, r \neq q, b_r=0}^{mN_t-1} LQ(k \rightarrow r)\right] \right\}}{\sum_{b: b_q=1} \left\{ p\left(\frac{Y_{k,n}}{b}\right) \exp\left[\sum_{r=0, r \neq q, b_r=0}^{mN_t-1} LQ(k \rightarrow r)\right] \right\}} \quad (6)$$

where $p(Y_{k,n}|b)$ is Gaussian distributed, and $LQ(r \rightarrow k)$ is the extrinsic information. Similarly, the term $(r \rightarrow k)$ in (9) is the message generated by br and passed to Yk . This message is given by

$$L_{Q(r \rightarrow k)} = L_{a,r} + \sum_{p=0, p \neq k}^{N_r-1} L_{R(p \rightarrow r)} \quad (7)$$

where $L_{a,r} = \ln[P_a(b_r = 0)/P_a(b_r = 1)]$ and $P_a(b_r)$ denotes the *a-priori* probability of the transmitted bit b_r . Finally, the decision variable, soft decision and hard decision for the r th bit are given in (8), (9) and (10), respectively:

$$L_{Q,r} = L_{a,r} + \sum_{k=0}^{N_r-1} L_{R(k \rightarrow r)} \quad (8)$$

$$\hat{b}_r = \tanh(0.5 \cdot L_{Q,r}) \quad (9)$$

$$\hat{b}_r = \begin{cases} 0, & L_{Q,r} \geq 0 \\ 1, & \text{otherwise} \end{cases} \quad (10)$$

A. LDPC Decoder

The message passing algorithm is also used to decode the LDPC code. The messages LU generated at the u^{th} code bit node and passed to the v^{th} check nodes are calculated as:

$$L_{U(u \rightarrow v)} = \sum_{p=0}^{N_r-1} L_{R(p \rightarrow u)} \quad (11)$$

The messages LV generated at the v^{th} check node and passed to the u^{th} code bit nodes are calculated as:

$$L_{V(v \rightarrow u)} = \prod_{\hat{u}=0, \hat{u} \neq u}^{d_c-1} \text{sign}(L_{U(\hat{u} \rightarrow v)}) \cdot \phi\left(\sum_{\hat{u}=0, \hat{u} \neq u}^{d_c-1} \phi(|L_{U(\hat{u} \rightarrow v)})|\right) \quad (12)$$

where $\phi(x) = -\log[\tanh(x/2)] = \log[(ex + 1)/(ex - 1)]$ and d_c is the row weight of parity check matrix.

IV. MESSAGE PASSING SCHEDULE AND INTERLEAVING

The short cycle problem degrades the BER performance which tends to improve very slowly with the number of iterations, even at very high SNR [14], [17]. With message passing algorithms, a cycle free factor graph guarantees an exact solution. The received frequency domain signals are fed forward to PPIC ICI canceller first. After the ICI cancellation, the signal are fed forward to MPD and then to LDPC decoder. As illustrated in Fig. 2 the factor graph of the PPIC canceller has no cycle, however, that of the MIMO detector and LDPC decoder has a lot of short cycles. In this paper, a properly designed frequency domain random interleaver has been used with LDPC code to solve this problem and to improve the system performance. The design criterion is that the coded bits in the same codeword have to be sent to different subcarriers after interleaving. Then, with a properly designed message passing schedule, the bit information is passed both in the space domain and the frequency domain. In this way, the short cycle problem is solved. In summary, seven steps are included in the message passing schedule.

1. The estimated soft data symbols, which are fed back from LDPC decoder, are exchanged between adjacent subcarrier nodes.
2. The ICI are reconstructed and cancelled from the received signals and then the ICI cancelled signals are fed forward to the MPD.
3. After the bit messages ($k \rightarrow q$) are generated by using (9) by the channel nodes at every subcarrier, these messages are passed to bit nodes.
4. After the messages ($q \rightarrow v$) are generated by using (14) and de-interleaved, these messages are passed to code bit nodes and then passed to check nodes of every LDPC decoder. Messages generated in a subcarrier are sent to different LDPC decoders.
5. The messages LV ($v \rightarrow u$) generated by check nodes using (15) are passed to code bit nodes and then

interleaved. The messages generated by every coded bit node of the same LDPC decoder are sent to bit nodes which belong to different subcarriers.

6. The messages ($u \rightarrow p$) are generated by bit nodes using (11) and then, passed to channel nodes.
7. Finally, the soft decision of data bits and data symbols are obtained by using (9) and then fed back to the PPIC canceller. By iterative detection, decoding and progressive ICI cancellation, the system performance can be jointly optimized.

V. COMPLEXITY

The order of computational complexity of the proposed MPD is $\{NcNr2mNt\}$. It is feasible when m , Nt , Nr and Nc are small. In a fading channel, the channel gains vary with time; hence the value of the information passed along the edges of the factor graph also varies with time.

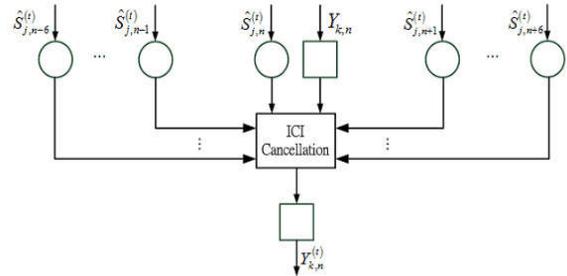


Fig. 2. An architecture of PIC ICI canceller.

The edges with low importance, which is corresponding to the deep-faded channel gain, can be ignored. Since the number of edges of the factor graph is reduced, the computational complexity of the proposed algorithm is also reduced. For PPIC ICI canceller, The computational complexity is in the order of $\{(NrNt + mNt)\}$. As the iterative process goes due to the progressive architecture, the computational complexity of each iteration of PPIC monotonically increases. This is different from the standard PIC architecture which has constant computational complexity in each iteration. Overall, the computation complexity of PPIC is lower than PIC. The system architecture of PPIC is also much simpler. Based on factor graph, the parallel structure of the proposed message passing MIMO data detector/decoder with PPIC ICI canceller is very suitable for VLSI implementation, especially for high speed analog detector/decoder where the iteration operation is actually a transient response and the high demand of computational complexity can be released.

VI. SIMULATION RESULTS OF BER PERFORMANCE

The BER performance of the proposed message passing algorithm on factor graph for data detection/decoding and ICI cancellation in bit-interleaved LDPC-coded MIMO-OFDM systems are simulated with $Nt = Nr = 2$ and QPSK modulation. Gallager code with codeword length 20 is used. The dimension of the parity check matrix of Gallager code is 15×20 with row weight $d_c = 4$ and column weight $d_v = 3$ [27]. The FFT size of OFDM modulator is 1024. An S -random interleaver [28] of length 8192, $S = 64$ is used after the LDPC encoding. The multipath channel model is the ITU vehicular A channel. The fading channel model used is the Jakes' model [29].

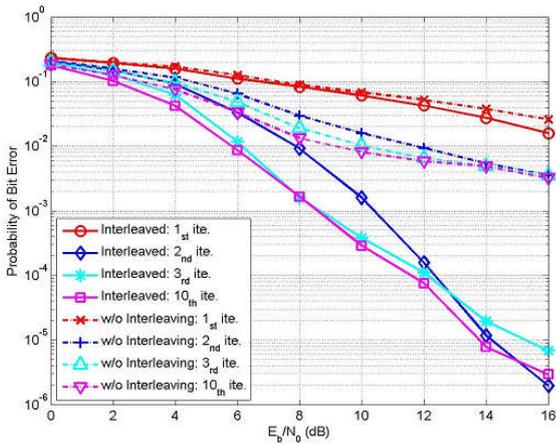


Fig. 3. With interleaving vs. without interleaving.

In the case of imperfect channel estimation, two different models are used to model the variance of channel estimation error: a) $\sigma^2 E$ is independent of the SNR. b) $\sigma^2 E$ is a decreasing function of SNR [30], [31]. The carrier frequency is 2.5 GHz, bandwidth is 10 MHz, sampling frequency is 11.2MHz, subcarrier spacing is 10.93 kHz, useful OFDM symbol duration is 91.43 μ s and the length of cyclic prefix is 1/8 [32]. The vehicular speeds are 350 km/h which are corresponding to maximum Doppler frequency 810 Hz and normalized Doppler frequency 0.07. The performances of the proposed algorithm with interleaving and without interleaving are compared in Fig. 3.

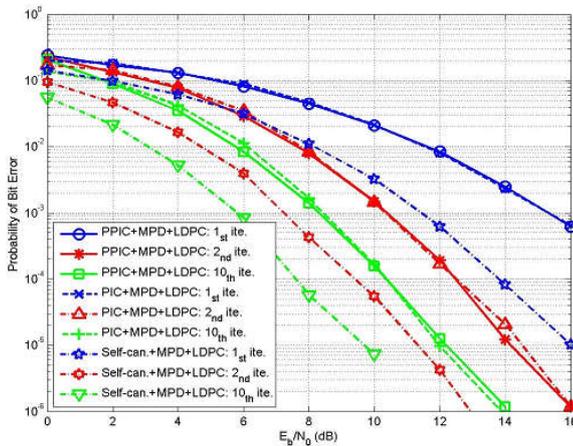


Fig. 4. Performance comparison of PPIC ICI canceller, PIC ICI canceller

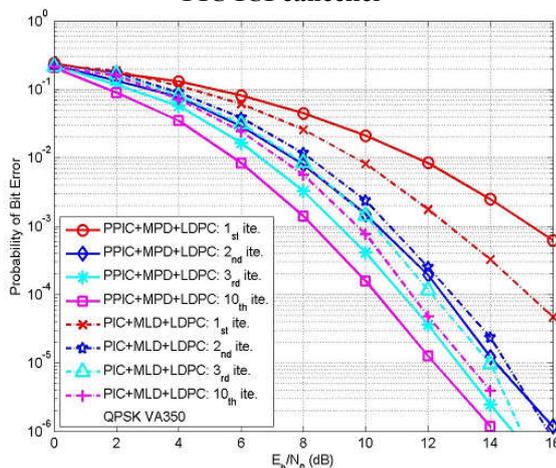


Fig. 5. PPIC+MPD+LDPC vs. PIC+MLD+LDPC.

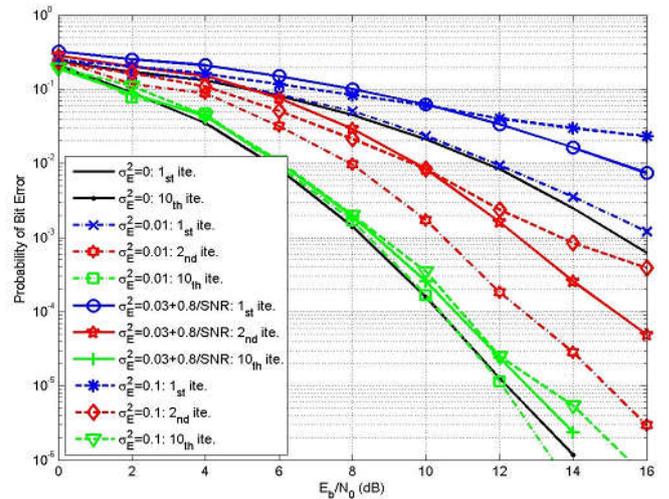


Fig. 6. Imperfect channel estimation: PPIC+MPD+LDPC.

It is obvious that the system performance is not improved with LDPC code without interleaving. This is because the cycle condition of the coded case is more serious than the uncoded case as the cycles not only exist in the space domain but also in the frequency domain. In order to optimize the system performance and deal with the short cycle problem, an S-random interleaver is used after the LDPC encoding. The performance comparison of MPD and MMSE-SIC is shown in Fig. 6. When the E_b/N_0 is smaller than 12 dB, the performance of MPD and MMSE-SIC are almost the same. However, when the E_b/N_0 is larger than 12 dB, MPD outperforms MMSE-SIC. When the BER is 10^{-5} , the performance of MPD is 1~2 dB better than MMSE-SIC at the 2nd and 3rd iteration. MMSE-SIC still has an error floor when the E_b/N_0 is larger than 12 dB. In Fig. 4, the performance of PPIC ICI canceller is compared with a standard PIC ICI canceller, which cancels 12 adjacent interfering subcarriers at every iteration, and an ICI Self-canceller.

The performances of PPIC ICI canceller and PIC ICI canceller are almost the same. Before the 6th iteration, although the PIC ICI canceller cancels more interfering subcarriers than PPIC ICI canceller, it does not outperform PPIC ICI canceller. The reason is that if the estimated data symbols are not accurate enough, the ICI may be increased instead of reduced after cancellation. For the ICI self-cancellation, a data pair ($S, -S$) is modulated onto two adjacent subcarriers where S is a complex data symbol. The ICI generated within a group can be self-cancelled on each other. The performance of ICI self-canceller is 2~3 dB better than PPIC ICI canceller at the price of half bandwidth efficiency. In Fig. 5, the performance of MPD is compared with maximum likelihood MIMO detector (denoted as MLD). It is obvious that PIC+MLD+LDPC performs better in the 1st iteration. However, after the 2nd iteration, PPIC+MPD+LDPC performs better. This is because MPD can exploit the fed back *a-priori* information to approach the maximum *a-posteriori* (MAP) solution but MLD only can approach the ML solution. The performance of the proposed algorithm with imperfect channel estimation is shown in Fig. 6. The variances of channel estimation error are 0.01, 0.1 and 0.03+ 0.8/SNR respectively.

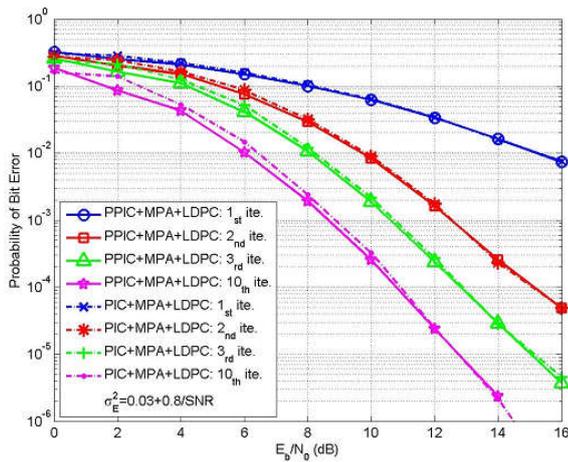


Fig. 7. Imperfect channel estimation: PPIC+MPD+LDPC vs. PIC+MPD+LDPC.

In each iteration, it is assumed that the channel coefficients are re-estimated and the variances of channel estimation error are assumed to be reduced 3 dB. When $\sigma^2 E = 0.01$, the performance has almost no degradation. Yet, if $\sigma^2 E = 0.03 + 0.8/SNR$, the performance may degrade 4 dB when BER is 10⁻² in the 1st iteration. In the 10th iteration, however, the performances are almost the same since $\sigma^2 E$ is reduced 3 dB in each iteration. The performances of PPIC and PIC in the case of imperfect channel estimation are compared in Fig. 7.

VIII. CONCLUSIONS

Based on factor graph, a joint design of message passing MIMO data detector/decoder with PPIC ICI canceller for OFDM-based wireless communication systems is proposed. The proposed algorithm suppresses inter-antenna interferences in space domain and cancels inter-carrier interferences in frequency domain iteratively and progressively. With a proper designed message passing schedule and random interleaver, the short cycle problem is solved. By iterative detection, decoding and progressive ICI cancellation, the system performance can be jointly optimized. The computational complexity of PPIC is relatively lower and the system architecture of PPIC is also simpler than PIC. The parallel structure of the proposed message passing MIMO data detector/decoder with PPIC ICI canceller is very suitable for VLSI implementation. In future high data rate, high mobility can be obtained for wireless MIMO-OFDM communication systems.

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